

## **Dr. Gaurav Bhandari**

**(PhD, M.Tech (Gold Medalist), B.Tech**

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### **CAREER OBJECTIVE:-**

To work with a reputed organization, which broadens my perspective and give me adequate opportunity to exploit my innate potential and experience so that I utilize the Knowledge which I have agglomerated till now.

### **PROFESSIONAL RESPONSIBILITIES:-**

- IQAC Coordinator, ICFAI Tech School
- Website coordinator, ICFAI University, Dehradun
- Digital Media coordinator, ICFAI University, Dehradun
- Department Coordinator Electronics and Communication, ICFAI Tech School
- Department Coordinator Mechatronics Engineering, ICFAI Tech School
- Apex Committee Member, ICFAI Tech School
- Time Table Coordinator, ICFAI Tech School
- Member of Board of Studies (ECE and Mechatronics Engineering) at ICFAI University, Dehradun

### **WORKSHOP ATTENDED:-**

- 1 day workshop on Virtual Labs sponsored by IIT Delhi held at ICFAI University Dehradun
- 2 Days workshop on Robotics (An Eyantra initiative) at IIT Bombay
- 2 Days Workshop on “Introduction to Robotics” at Shivalik College of Engineering, Dehradun
- 4 Days workshop on soft computing techniques using Matlab through ICT
- 1 Day Workshop on Latex at ICFAI University, Dehradun
- 5 Days Workshop on Teaching Methodology at IFHE Hyderabad

### **QUALIFICATION:-**

- Ph.D.in Electronics and Communication from The ICFAI University, Dehradun .
- Master of Technology (*Gold Medalist*) in Digital Communication from Uttarakhand Technical University, with Aggregate Percentage in M.Tech. 80.2%. with Honors
- Bachelor of Technology in Electronics & Communication from Uttar Pradesh Technical University, With Aggregate Percentage in B.Tech:- 68.1%.

### **RESEARCH CREDENTIALS:-**

- Published paper title AI-Enabled Computational Techniques for Cancer Diagnosis in 2021 IEEE 8th Uttar Pradesh Section International Conference on Electrical, Electronics and Computer Engineering (UPCON).
- Published a Paper title “Scalable Optimized link state heuristic in cross layering with QoS forNGN” in International Journal of Scientific and Technology Research in March 2020.
- Published a paper title a review paper on visible light communication: architecture, standardization,

application and research issues in International Journal For Technological Research In Engineering in 2020.

- Published a paper titled analysis of wireless communication network from 0g to 5g with the future perspectives in IUDJournal Antardrishti in May 2021.
- Published a Research Paper Title “An Analysis of self convolutional windows using Matlab” in IUD Journal Antardrishti in October 2020.
- Presented a paper title “Self Convolution Windows: A critical Analysis” in International conference held at Shiksha “O” anusandhan on 22-23 September 2018.
- Published a paper title “Pulse shape visible light communication system analysis for out band interference” in International conference held at Shiksha “O” anusandhan on 22-23 September 2018.
- Published a paper entitled “I<sup>2</sup>SP hybrid pulse shape: Modified I<sup>2</sup>SP pulse by using weighting technique and its impact on SC-FDMA system” in International journal of Computer Science and Engineering (IJCSE), June 2017,
- Published a paper entitled “An Improved Pulse Shape Combined with Companding transform to Optimize SC-FDMA Performance” in International journal of Computer Science and Information security, IJCSIS, Vol. 14 No.11, 2016.
- Published a paper entitled “OBR Analysis in SC-FDMA system using Modified Raised cosine pulse” in International journal of Advanced Information Science and Technology, IJAIST, August 2016, Vol 52, No.52
- Published a paper entitled “Analyzing the effect of Modulation order and FFT size on PAPR for SC-FDMA and OFDMA system” in International journal IJAEET Volume 3 No. 3 November 2014
- Published a paper entitled “Analyzing CFO Estimation Techniques Under Different Guard Interval Length For Multicarrier Transmission System” in IJEECS , Volume 4 Special issue (ETET ‘14)
- Published a paper title “Analyzing the Effect of Modulation Order and Sub-bands on PTS and SLM PAPR Reduction Technique in Various 802.11 Standards Using Different Modulator” in International journal, IJSER Volume 4 issue 5, May 2013.
- Published a Research paper entitled “Comparative Study and Survey between Intercarrier Interference Reduction Technique” in International journal, IJSER Volume 4, Issue 3, March 2013 Edition.
- Published a Research paper entitled “Review and survey of optimum clustered pilot sequence for OFDM system under rapidly time varying channel” in International journal, IJES Volume 2 issue 1.

### **Work Profile**

Currently Working as an Assistant Professor in ICFAI University Dehradun in Electronics and communication and Mechatronics Engineering Department. I am looking after the Department coordination for Electronics and communication and Mechatronics Engineering where I am looking after the smooth and proper functioning of ECE Department and Mechatronics Department.

## **SUBJECT TAUGHT:-**

### **B.Tech**

- 1: Basic Electronics
2. Analog Communication
- 3: Digital Electronics
- 4: Electronic Devices and Circuits
5. Communication
6. Signals and systems
- 7: Semiconductor Materials & Devices
8. Wireless Communication
- 9: Optical Fiber Communication
9. Electrical Science-I
10. Digital Communication
11. Data Communication Systems
12. Fundamental of Electrical Engineering
13. Digital Signal Processing
14. Spread Spectrum Systems
15. Fundamental of Electrical Engineering

### **M.Tech**

1. Information theory and coding
2. Spread Spectrum System

## **ELECTRONICS COMPETENCY:-**

- Ph. D Thesis:- Efficient Techniques to Reduce OBR for SC-FDMA Based Cellular System.
- M.Tech Project:- Performance Analysis of OFDM-QPSK under AWGN & Rayleigh fading.
- B.Tech Project:- Implementing Basic Time Division Time Switch using VHDL.
- Industrial Training:- 6 Weeks Training on Networking at ONGC(Tel Bhavan).

## **COMPUTER COMPETENCY:-**

- Languages Known:- C, MATLAB, HFSS, Keil software, Arduino, Proteus
- Operating Systems:- Windows 98/2000/ XP, Linux
- Electronics Software:- Electronics workbench, Circuit maker software, Modelsim

## **PROJECT GUIDED:-**

**Ph.D** (supervisor):-03

**M.Tech**:-02

**B.Tech**:-12

**I Declare that the Information quoted above is true to best of my knowledge**

**Place: - Dehradun**

**Dr. GAURAV BHANDARI**