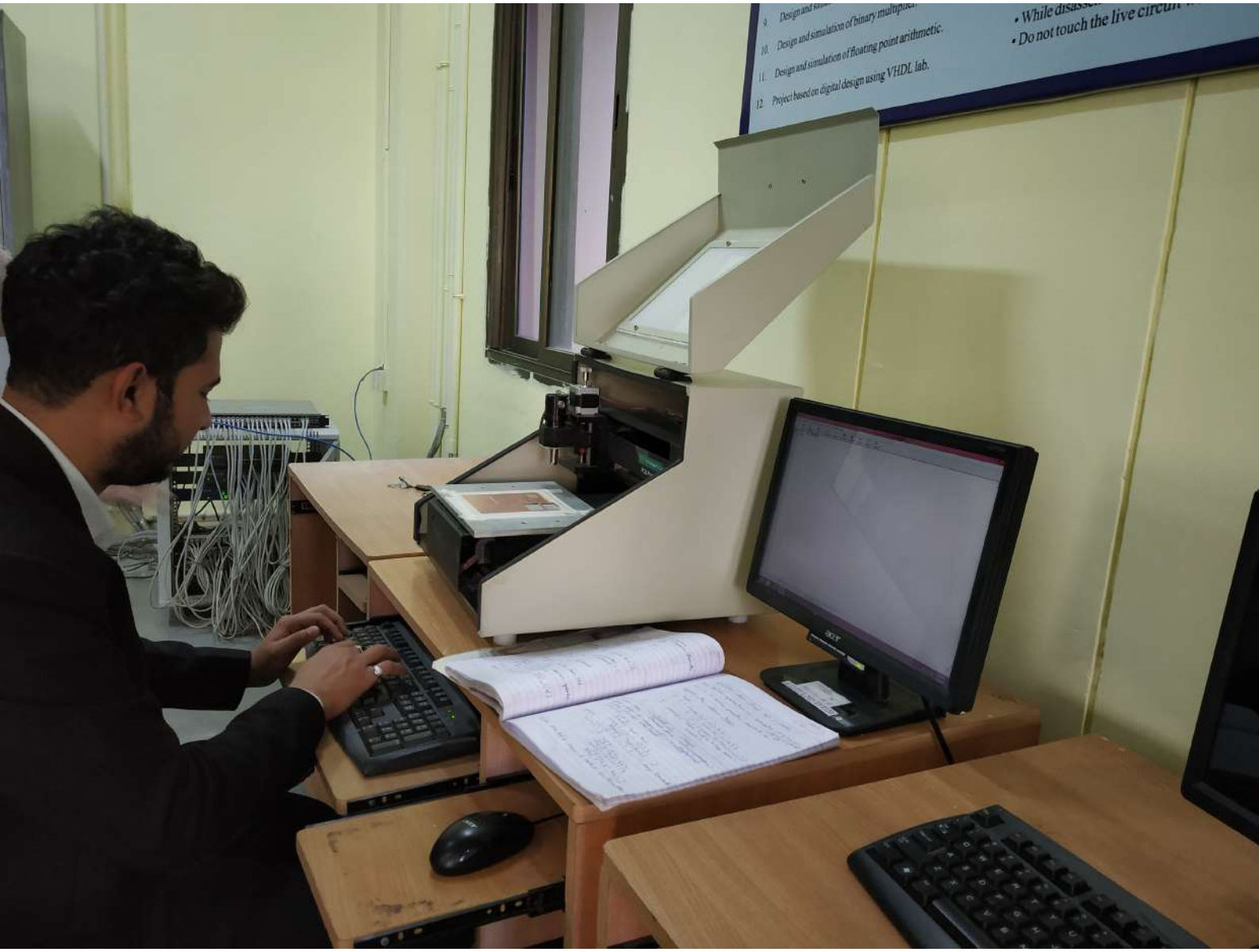

Analog System Lab Kit PRO
PMLK
Power Management Lab Kit
LDO Experiment Book


amazon.in


10. Design and simulation of floating point arithmetic.
11. Design and simulation of VHDL lab.
12 Project based on digital design using VHDL lab.
• Do not touch the live circuit


Name of Experiment
• Do not bring bags inside the laboratory.
• Do not eat or drink in the laboratory.
• Replace insulated probes and leads at the first sign of deterioration.
• Avoid stepping on electrical wires or any other computer cables.
• Never assume a circuit is off. Double check it.
• Do not remove grounds and do not use adaptors that defeat the equipments ground.
• Avoid wearing rings, bracelets, and similar metal items when working around exposed electric circuits.
• Do not touch, connect or disconnect any plug or cable without your lecturer/laboratory technician's permission.
• Do not work on equipment before you know proper procedures and are aware of any potential safety hazards.
• Use protective clothing and safety glasses when handling high vacuum devices and cathode ray tubes
• Unauthorized experiments are not allowed in the Laboratory.
• While disassembling a circuit, first remove the source of power.
• Do not touch the live circuit with bare hand.


ICFAI Univer
Recognized under the section 2(
ICFAI Tech Schoo
DIGITAL DESIGN USING VHDL (ECL
Name of Experiment
1. Design and simulation of logic gates.
2. Design and simulation of X-OR gate using NAND & NOR gates
3. Design and simulation of half adder & half subtractor.
4. Design and simulation of full adder & full subtractor.
5. Design and simulation of multiplexer & de-multiplexer.
6. Design and simulation of encoder & decoder.
7. Design and simulation of flip flops.
8. Design and simulation of up-down counter decade counter.
9. Design and simulation of different shift registers.
10. Design and simulation of binary multiplier.
11. Design and simulation of floating point arithmetic.
12. Project based on digital design using VHDL lab.
• General Instructions to the Students
• Report any broken plugs or exposed electrical
lecturer/laboratory technician immediately.
• Keep the equipments in proper place after work to
area neat and tidy.
• Do not bring bags inside the laboratory.
• Do not eat or drink in the laboratory.
• Replace insulated probes and leads at the fir
deterioration.
• Avoid stepping on electrical wires or any other comput
• Never assume a circuit is off. Double check it.
• Do not remove grounds and do not use adaptors that
equipments ground.
• Avoid wearing rings, bracelets, and similar metal iter
working around exposed electric circuits.
• Do not touch, connect or disconnect any plug or cable
your lecturer/laboratory technician's permission.
• Do not work on equipment before you know proper proc
and are aware of any potential safety hazards.
• Use protective clothing and safety glasses when handling
vacuum devices and cathode ray tubes.
• Unauthorized experiments are not allowed in the Laborato
• While disassembling a circuit, first remove the source of pow
• Do not touch the live circuit with bare hand.