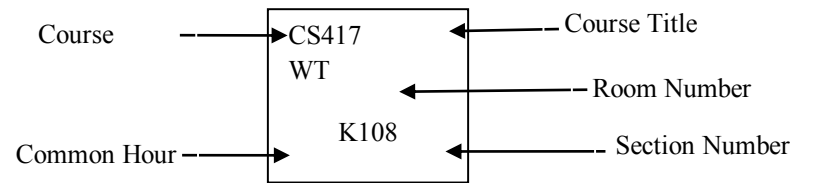


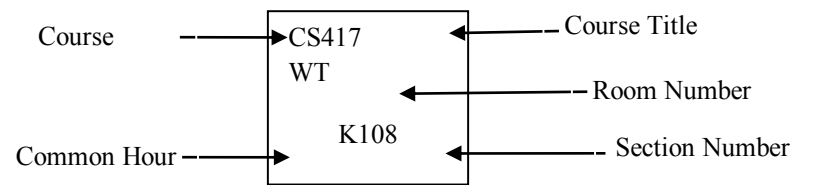
Dr. R. C. Ramola (RCR)

	1 9.25am-10:15 am	2 10:20am - 11:10am	3 11:15am - 12:05pm	4 12:10pm - 1:00pm	5 01:05pm- 1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon			EC404 FOO B304 SECI						
Tue			MEL602 AVT RCR/VRK						
Wed			EC404 FOO B304 SECI						MEL602 AVT RCR/VRK
Thu			EC404 FOO B304 SECI						MEL602 AVT RCR/VRK
Fri			EC404 FOO B304 SECI					MEL602 AVT RCR/VRK	



Dr. Sandip Vijay (SV)

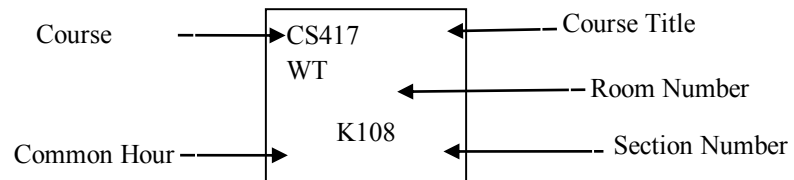
	1 9:25am-10:15 am	2 10:20am - 11:10am	3 11:15am - 12:05pm	4 12:10pm - 1:00pm	5 01:05pm- 1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon		MEL501 MDC SV		EC315/ CTS ME315 G202 EC/ME 3rd Yr.					
Tue		MEL501 MDC SV		EC315/ CTS ME315 G202 EC/ME 3rd Yr.					
Wed		MEL501 MDC SV		EC315/ CTS ME315 G202 EC/ME 3rd Yr.					
Thu				EC315/ CTS ME315 G202 EC/ME 3rd Yr.					
Fri		MEL501 MDC SV				VPD Lab SV			



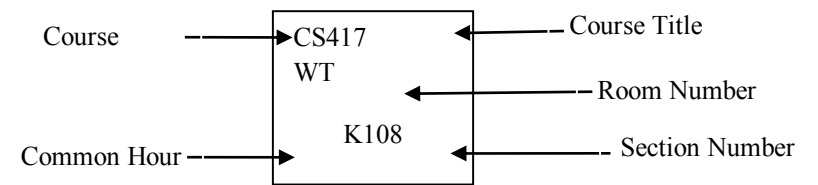
Mr. Vipin Patat (VP)

	1 9.25am-10:15 am	2 10:20am - 11:10am	3 11:15am - 12:05pm	4 12:10pm - 1:00pm	5 01:05pm- 1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon				EC407 MCA B304 SEC1			CS/EC211 DLD C301 SEC1		
Tue		EC407 MCA B304 SEC1						Microcontroller Apps. Lab VP L205	
Wed				CS/EC211 DLD C301 SEC1		EC407 MCA B304 SEC1			
Thu	DLD Lab 2 nd Year (CS/EC)			CS/EC211 DLD C301 SEC1					
Fri			CS/EC211 DLD C301 SEC1			EC407 MCA B304 SEC1			

Mr. M. Raja Rao (RR)

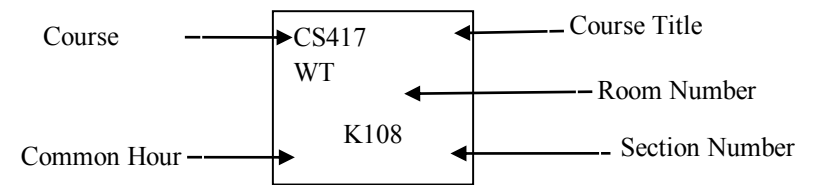


	1 9:25am-10:15 am	2 10:20am-11:10am	3 11:15am-12:05pm	4 12:10pm-1:00pm	5 01:05pm-1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon	ES211 ES1 B302 SEC1		EC312 DSP C305 SEC1				EC401 TSSN B304 SEC1		
Tue			ES211 ES1 B302 SEC1				EC401 TSSN B304 SEC1		
Wed	EC312 DSP C305 SEC1		ES211 ES1 B302 SEC1					Digital Signals Processing Lab	
Thu	EC312 DSP C305 SEC1			EC401 TSSN B304 SEC1				ES211 ES1 B302 SEC1	
Fri	EC312 DSP C305 SEC1						EC401 TSSN B304 SEC1		



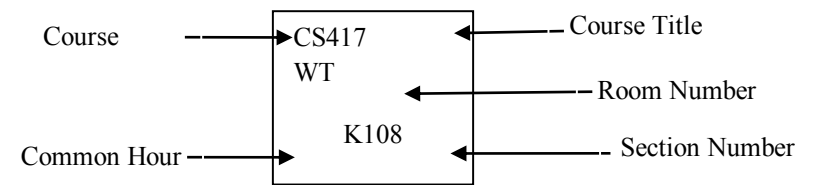
Mr. Virendra Kumar (VRK)

	1 9:25am-10:15 am	2 10:20am-11:10am	3 11:15am-12:05pm	4 12:10pm-1:00pm	5 01:05pm-1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon		EC311 AC C305 SEC1					EC314 EMFW C305 SEC1		
Tue	EC311 AC C305 SEC1		MEL602 AVT RCR/VRK			EC314 EMFW C305 SEC1			
Wed						EC311 AC C305 SEC1			MEL602 AVT RCR/VRK
Thu		EC311 AC C305 SEC1				EC314 EMFW C305 SEC1			MEL602 AVT RCR/VRK
Fri				EC314 EMFW C305 SEC1		Analog Communication Lab		MEL602 AVT RCR/VRK	



Mr. Anilesh(AN)

	1 9:25am-10:15 am	2 10:20am-11:10am	3 11:15am-12:05pm	4 12:10pm-1:00pm	5 01:05pm-1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon		EC420 ADVD B304 SEC1				EC423 DDH B304 SEC1		Digital Design with HDL Lab	
Tue			EC423 DDH B304 SEC1			EC420 ADVD B304 SEC1		VPD Lab AN	
Wed		EC423 DDH B304 SEC1		EC420 ADVD B304 SEC1				MEL601 HDL AN	
Thu		MEL601 HDL AN				EC420 ADVD B304 SEC1		MEL601 HDL AN	
Fri		EC423 DDH B304 SEC1		MEL601 HDL AN				Analog and Digital VLSI Design Lab	



Mr. Gaurav Bhandari(GB)

	1 9.25am-10:15 am	2 10:20am-11:10am	3 11:15am-12:05pm	4 12:10pm-1:00pm	5 01:05pm-1:55pm	6 2:00pm-2:50pm	7 2:55pm-3:45pm	8 3:50pm-4:40pm	9 4:40pm-5:30pm
Mon	CS315 DCS C304 SEC1					EC313 ECA C305 SEC1			
Tue	CS315 DCS C304 SEC1			EC424 MTN B304 SEC1			EC313 ECA C305 SEC1		
Wed		EC313 ECA C305 SEC1		CS315 DCS C304 SEC1			EC424 MTN B304 SEC1		
Thu		EC424 MTN B304 SEC1						Electronic Circuits Analysis Lab	
Fri		EC313 ECA C305 SEC1		EC424 MTN B304 SEC1		CS315 DCS C304 SEC1			